

WHAT IS CLAIMED IS:

1. A control chip built inside an integrated circuit for reducing electromagnetic interference, wherein the control chip is able to spread out the frequency of an electromagnetic interference signal according to an algorithm.

5 2. The control chip of claim 1, wherein the control chip picks up the algorithm from an external bus.

3. A control chip for reducing electromagnetic interference, comprising:

 a software phase lock loop built inside the control chip for receiving a clock signal and spreading out the frequency of an electromagnetic

10 interference signal according to an algorithm; and

 a bus coupled to the software phase lock loop for inputting the algorithm.

4. The control chip of claim 3, wherein the frequency of the electromagnetic interference signal and the spread out width at that frequency is set by the algorithm

15 within the software phase lock loop.

5. An application specific integrated circuit for reducing electromagnetic interference, comprising:

 a first input terminal for receiving a clock signal;

 a second input terminal for receiving an algorithm; and

20 a software phase lock loop coupled to the first input terminal and the second input terminal for spreading out the frequency of an electromagnetic interference signal according to the clock signal and the algorithm.

6. The application specific integrated circuit of claim 5, wherein the frequency of the electromagnetic interference signal and the required spread out width at that frequency are set by the algorithm within the software phase lock loop.

7. A method of reducing the strength of an electromagnetic interference signal,
5 comprising the steps of:

receiving an algorithm;

determining a specified frequency of the electromagnetic interference signal and a corresponding spread out width at that frequency according to the algorithm; and

10 spreading out the electromagnetic interference signal according to the spread out width using the specified frequency as the center of spreading.